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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/707,536	12/19/2003	Munehiro Karasudani	22040-00023-US	1535	
30678	7590 11/26/2004	EXAMINER		INER	
CONNOLLY BOVE LODGE & HUTZ LLP			TRAN, T	TRAN, THIEN F	
SUITE 800 1990 M STREET NW		ART UNIT	PAPER NUMBER		
WASHINGTON, DC 20036-3425			2811		
			DATE MAILED: 11/26/2004		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/707,536	KARASUDANI, MUNEHIRO				
Office Action Summary	Examiner	Art Unit				
	Thien F Tran	2811				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period was a reply within the set or extended period for reply will, by statute, any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).				
Status ·		•				
1) Responsive to communication(s) filed on	_•					
2a) ☐ This action is FINAL. 2b) ☒ This	☐ This action is FINAL. 2b)☑ This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.				
Disposition of Claims						
 4) ☐ Claim(s) 1-3 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1 is/are rejected. 7) ☐ Claim(s) 2 and 3 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or 	·					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) acce						
Applicant may not request that any objection to the	-					
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	•					
Priority under 35 U.S.C. § 119		•				
a) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Application in the second	on No ed in this National Stage				
Attachment(s)	· ·					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail Da	·				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 12/22/2003.		Patent Application (PTO-152)				

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DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Rao (USPN 5,761,694).

Rao discloses the claimed integrated circuit that integrates a plurality of functional blocks (203a, 203b, 201a, 201b) within a single semiconductor chip (Figure 2), comprising: a plurality of decoding circuits (204a, 204b) within said semiconductor chip, for which address lines and data lines are connected to an input terminal, an address signal inputted from said address lines is decoded, and the data inputted from said data lines is outputted according to the results of the decoding; and signal lines with a few bits including said address lines and said data lines, wherein said signal lines are wired to said plurality of the decoding circuits.

Allowable Subject Matter

Claims 2 and 3 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: prior art references do not teach or render obvious an integrated circuit in a

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single semiconductor chip wherein the plurality of the decoding circuits are arranged in the same number of the plurality of functional blocks.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thien F Tran whose telephone number is (571) 272-1665. The examiner can normally be reached on 8:30AM - 5:00PM Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

tt November 17, 2004

THIENTRAN
PRIMARY EXAMINER

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